Approaches to Designing a Wireless Sensor Network Node for IoT Solution

Anton Sukov

dept. Design and Operation of Electronic Devices Kharkiv National University of Radioelectronics Kharkov, Ukraine anton.sukov@nure.ua

Vyacheslav Olizarenko

dept. Design and Operation of Electronic Devices Kharkiv National University of Radioelectronics Kharkov, Ukraine viacheslav.olizarenko@nure.ua

Abstract—The object of the research is the hardware component for building a test platform for wireless sensor networks. The aim of the work is to develop a software and hardware test platform for wireless sensor networks. As a result of the analysis, the node structures, wireless sensor network modules, CC2530 peripherals were analyzed. A module based on the CC2530 PA was chosen as the hardware. Given to optimize the structure of the node for as one of approach to designing a wireless sensor network node. Also given report about difference in approach to designing nodes and uses areas. All developments will receive life in the educational process of the department. Sample production is already underway.

Keywords—approach, CC2530, software and hardware test platform, node, wireless sensor network

I. INTRODUCTION

The Wireless sensor networks (WSN) systems have a lot of problems like security, energy consumption, heterogeneity and other disadvantages that need be solved [1]. Therefore, it is quite difficult to design a sensor network node so that it satisfies the necessary criteria for optimality. If such a node is also used for testing and training, then additional requirements for the construction will be propose to, for example, as in articles of designing microprocessor systems [2] or embedded control systems [3]. Energy monitoring [4] is a key factor for the successful prolongation of life times each nodes in wireless sensor network, for examples reducing the power consumption of nodes [5]. Therefore, can set the task to optimize the structure of the node for as one of approach to designing a wireless sensor network node.

The purpose of the work is to develop a software and hardware test platform for the Internet of Things.

The first part the development of a test IoT layout, namely, a communication module based on the CC2538 radio transceiver.

The second part development of a test IoT layout. Communication module based on the CC2530 radio transceiver.

Alexey Zayanchukovsky

dept. Design and Operation of Electronic Devices Kharkiv National University of Radioelectronics Kharkov, Ukraine oleksii.zaianchukovskyi@nure.ua

Pavlo Galkin ORCID 0000-0002-0558-6448 dept. Design and Operation of Electronic Devices Kharkiv National University of Radioelectronics Kharkov, Ukraine pavlo.halkin@nure.ua

The third part is development of a test IoT layout, namely, a communication module based on the ESP8266 radio transceiver.

II. CC2530 NODE FOR IOT

The CC2530 contains many peripherals that provide everything to develop various applications. The debugging interface uses I/O ports P21 (data) and P22 (synchronization) in debug mode. In this case, in the debug mode, the other 19 ports pins can be active, which gives a great opportunity to debug the module, while at the same time it can be connected to quite a few different devices. In other cases, the ports for debugging can work in the normal GPIO mode. In general, the debugging interface allows you to track all processes that occur in the module when it is in an active state (user program execution), and also to change the parameters of these processes (for example, values in registers) and in real time to observe changes in the work of the module.

As a result of the analysis, the node structures, wireless sensor network modules, peripherals CC2530 were analyzed. A module based on CC2530 PA was chosen as the hardware. Elements such as:

- LEDs ;
- push buttons;
- photoresistor;
- humidity sensor;
- ultrasonic sensor;
- exits to the outer periphery.

II International Scientific and Practical Conference Theoretical and Applied Aspects of Device Development on Microcontrollers and FPGAs

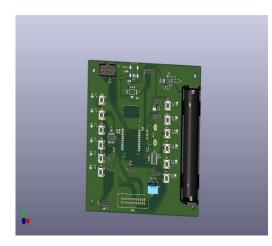


Fig. 1. Developed PCB board base on CC2530.

III. CC2538 NODE FOR IOT

The CC2538xFnn is the ideal wireless microcontroller System-on-Chip (SoC) for high-performance ZigBee applications. The device combines a powerful ARM Cortex-M3-based MCU system with up to 32KB onchip RAM and up to 512KB on-chip flash with a robust IEEE 802.15.4 radio. This enables the device to handle complex network stacks with security, demanding applications, and over-theair download.

As a result of the analysis, the node structures, wireless sensor network modules, CC2538 peripherals were analyzed. A module based on the CC2538 was chosen as the hardware. As elements of the periphery, the following elements were chosen:

- LEDs;
- push buttons;
- Temperature, humidity and pressure sensor in the one case;
- Buzzer;
- hall sensor;
- lighting sensor;
- voltage divider with adjustable resistor.

A contact switch is selected to switch between the peripheral elements, and electrical switch for switching between external and internal peripheral (Fig. 2).

IV. ESP8266 NODE FOR IOT

The ESP8266 is a low-cost Wi-Fi microchip, with a full TCP/IP stack and microcontroller capability, produced by Espressif Systems in Shanghai, China.

Proposed developed PCB board base on ESP8266 shown on Fig.3.

V. CONCLUSION

The topic of the modern concept of IoT and the possibilities of the ZigBee protocol were considered. Similar devices were considered. The selected module and its

capabilities were considered. The model is developed, its separate possibilities and components are considered.



Fig. 2. Developed PCB board base on CC2538.



Fig. 3. Developed PCB board base on ESP8266.

REFERENCES

- [1] C. Alvarado, F. Bosquez, Palacios and L. Córdoba, "Low-energy Adaptive Clustering Hierarchy protocol and optimal number of cluster head algorithm in a randomized wireless sensor network deployment," 2017 International Conference on Electrical, Electronics, Communication, Computer, and Optimization Techniques (ICEECCOT), Mysuru, 2017, pp. 1-4. doi: 10.1109/ICEECCOT.2017.8284632.
- [2] V. Semenets, "Technical aspects for development laboratory base for learning FPGA and microcontroller systems," 2009 10th International Conference - The Experience of Designing and Application of CAD Systems in Microelectronics, Lviv-Polyana, 2009, pp. 145-145.
- [3] P. Galkin, "Razrabotka laboratornogo kompleksa po izucheniyu vstraivaemyih sistem upravleniya i promyishlennoy avtomatizatsii [Development of a laboratory complex for the study of embedded control systems and industrial automation]," Materials of the 21st International Youth Forum "Radio Electronics and Youth in the 21st Century", April 25-27, 2017 Conference "Automated systems and computerized technologies of radio-electronic instrument-making", Kharkiv, KNURE, vol. 2, P.94-95. (In Russian).
- [4] P.V. Galkin. "Analiz energopotrebleniya uzlov besprovodnih sensornih setei [Analysis of power consumption of nodes of wireless sensor networks]," ScienceRise, no.2 pp 55-61, 2014. (In Russian).
- [5] P. Galkin, "Model of Reducing the Power Consumption for Node of Wireless Sensor Network in Embedded Control Systems," 2018 International Scientific-Practical Conference Problems of Infocommunications. Science and Technology (PIC S&T), Kharkiv, Ukraine, 2018, pp. 252-256. doi: 10.1109/INFOCOMMST.2018.8631891.

II International Scientific and Practical Conference Theoretical and Applied Aspects of Device Development on Microcontrollers and FPGAs