

Using FPGA Structures For Automotive Electronics

Vladimir Karnaushenko
ORCID 0000-0001-7744-2569
Department of Microelectronics,
electronic devices and appliances
Kharkiv National University of Radioelectronics
Kharkiv, Ukraine
vladimir.karnaushenko@nure.ua

Alexander Borodin
ORCID 0000-0001-7744-2569
Department of Microelectronics,
electronic devices and appliances
Kharkiv National University of Radioelectronics
Kharkiv, Ukraine
alexander.borodin@nure.ua

Abstract—As microelectronics becomes increasingly important in automotive systems, transport electronics developers are increasingly relying on FPGA programmable structures to create applications with better performance and flexible architectures.

Keywords—electronic control units, transport applications, programmable logic blocks, automotive information technology, hardware, digital signal processing

I. IMPROVING COMPUTING PERFORMANCE FOR TRANSPORT

Although cars have a long and rich history, electronics have been used extensively in transport applications relatively recently, becoming an integral part of the automotive world only in recent decades. Today, car companies compete fiercely not only with each other, but also with the latest and most modern technologies [1].

Currently, manufactured cars rely on many sensors to measure many internal and external variables that could affect the car's driving behavior, as well as additional parameters such as visibility and passenger comfort. Depending on their level of sophistication, sensors can be classified from simple sensors that directly measure individual physical parameters (eg ambient light sensors and temperature sensors) to complex intelligent sensors that determine environmental parameters using broad-spectrum signals (eg radio frequency, radars and light, video); in addition to measurements, they perform data processing and have the ability to perform drives [2].

Using processor-based electronic control units (ECUs), it is difficult to keep up with consumer electronics due to the long cycles of chip development and strict standards of reliability and quality applied to the automotive industry. The automotive industry uses increasingly sophisticated electronic systems to offer the driver better safety and efficiency. Programmable arrays of valves (FPGAs) can play an important role in filling this gap, providing up-to-date performance and high flexibility to system architects to customize projects through a flexible (programmable) electronic circuit structure.

The main goal in automotive design is to reduce the total number of ECUs, as they increase the overall cost of the vehicle and reduce reliability. Thanks to the latest advances

in FPGA structures, it is now possible to combine electronic components inside the car more intelligently.

For example, the implementation of a purely hardware processor architecture is a problem that needs to be addressed urgently. However, one possible solution to these interferences is a hybrid approach that combines processors and ECUs with FPGA-based chip systems (SoC), Fig.

FPGAs contain an array of programmable logic blocks, such as built-in memory, digital signal processing units (DSPs), and high-speed receivers. With FPGAs, the automotive system becomes easily scalable with minimal hardware changes.

In this way, FPGAs create opportunities for automotive original manufacturers and suppliers to more effectively build innovative safety programs, such as adaptive cruise control, driver assistance, collision avoidance and blind spot warning.

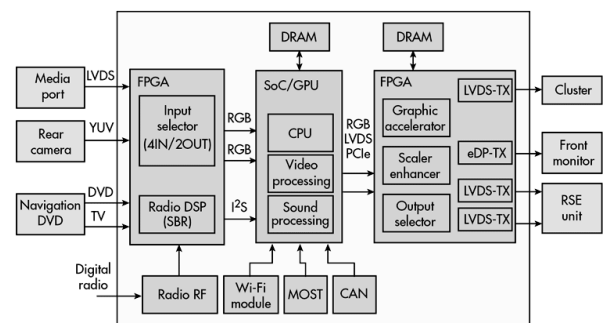


Fig. 1. Hardware processor architecture

As the name implies, driver assistance includes features such as reversing cameras, three-dimensional surveillance cameras, lane departure warning systems, pedestrian detection and more [3].

II. HARDWARE AND SOFTWARE

FPGA is a semiconductor device based on a matrix of programmable logic blocks, which are determined by their functionality. This feature distinguishes FPGAs from specialized integrated circuits (ASICs) designed for applications designed for specific design tasks. ASICs and FPGAs have a number of key benefits that need to be carefully evaluated before making a decision. With the

development of unmatched logic density and many other features such as digital signal processing, clock speed and high-speed serial bus, FPGAs become a reliable helper for almost any type of design.

For example, automotive infotainment systems are of great importance in modern vehicle design and have a significant impact on the sale of global vehicles. In these systems, it is important to choose the right main system processor to differentiate the user interface with the latest graphics. With several models to support, you may have to choose different SoCs due to system variations and the emergence of new interoperability technologies.

Thanks to FPGA, the system becomes easily scalable, which allows you to update the firmware remotely to support more manufacturers, regions and models with minimal hardware modifications. You can use the FPGA to support any combination of I / O interfaces [4].

The most important design factor of FPGAs is that they are programmable logic devices. Of course, CPU software can be upgraded, but the same cannot be said for computer architecture. On the other hand, FPGAs can be configured or reprogrammed to perform various functions an infinite number of times. In many recent cars, the software tracks many functions during operation. For example, Tesla models already support software updates remotely.

Thanks to this feature, FPGAs are able to constantly support the original software of manufacturers with the latest versions of programmable or customized hardware architecture systems. Such software updates can be applied to various car features, which may include more FPGA-oriented structures as they become more powerful, smaller and cheaper [5].

Connected vehicles are able to analyze information in real time to provide new information to car users, optimizing their experience. Meanwhile, IoT connectivity can help develop new development models for the automotive market by transforming the relationship between automakers and drivers.

III. AUTOMOTIVE INFORMATION TECHNOLOGY

As more IoT technologies are implemented in automotive applications, this is a prerequisite for the convergence of innovations - especially in the electronics industry. However, experienced engineers know that there is a learning curve when using something new that comes into direct conflict with less development time. In turn, this increases the project risk.

For this reason, designers tend to reuse technologies that are already well known or have been used before. Over time, this philosophy transforms some architecture into widely used industry standards, while most others are used only in narrow market niches.

IoT engineers will have to deal with significant issues such as energy efficiency and management of incompatible interfaces. The FPGA-based design approach can help solve these problems by offering a fully functional hardware platform for very low-power IoT applications.

When researching which 32-bit processor will best serve customers, many companies realize that the standard industry architecture offers significant benefits to the owner. Standard industry processors, as a rule, have a wide range of development tools, a large number of available software codes and designers who have the knowledge and experience to use them. Such benefits accelerate project development time (and therefore time to market) and also reduce a project risk, which in turn provides users with higher value-added solutions [6].

On this front, the ARM Cortex – M1 processor, designed from scratch for use in FPGAs, stands out. One of the main functions - it helps to minimize the amount of resources needed to meet the requirements of the developer. For example, debugging functions can be enabled or removed. Operating system extensions for system timers and software interrupts are not required. Cortex-M1 works with most basic FPGAs, which means that switching from one FPGA device to another requires minimal effort.

IV. CONCLUSIONS

FPGAs are now implemented separately or together with processors in many automotive systems, as they provide more efficient and faster solutions for hundreds of ECUs in a vehicle. They provide higher performance without consuming more energy, and improve customization and scaling capabilities [7].

FPGAs can also help reduce overall car ownership costs by integrating and / or reducing the number of external components, speeding up time to market, and consolidating accelerated project development.

In addition, thanks to innovative and cost-effective imaging solutions, FPGAs support the implementation of even more automotive features. Finally, they often offer a more cost-effective option in programs such as traffic control systems or engines. At present, it is expected that the design needs of the growing hybrid and electric vehicle industry will also focus on expanding the FPGA market.

REFERENCES

- [1] FPGA Considerations for Automotive Applications. Rick Nicholson, Michael Gabrick, Frank Winters. Conference: SAE World Congress & Exhibition. DOI 10.4271/2006-01-0368
- [2] M. Maurer, "Forward Collision Warning and Avoidance." In: A. Eskandarian (Ed.), Handbook of Intelligent Vehicles, Springer London, 2012.
- [3] Three decades of driver assistance systems: Review and future perspective. K Bengler, K Dietmayer, B Farber, M Maurer, C Stiller, H Winner – IEEE Intelligent Transportation Systems Magazine vol. 6, no. 4, Winter 2014, pp. 6-22.
- [4] Deep multi-modal object detection and semantic segmentation for autonomous driving: Datasets, methods, and challenges, – D Feng, C Haase-Schuetz, L Rosenbaum, H Hertlein – IEEE Transactions on Intelligent Transportation Systems, 2020.
- [5] A parallel implementation of sequential minimal optimization on FPGA. DH Noronha, MF Torquato, MAC Fernandes, – Microprocessors and Microsystems 69, 138-151.
- [6] Markvollrath; Schleicher, S.; Gelau, C. The influence of Cruise Control and Adaptive Cruise Control on driving behaviour—A driving simulator study. *Accid. Anal. Prev.* 2011, 43, 1134–1139.
- [7] Field Programmable Counter Arrays Integration with Field Programmable Gates Arrays. Vladimir Karnaushenko, Alexander Borodin. – Theoretical and Applied Aspects of Device Development on Microcontrollers and FPGAs, MC&FPGA. – 2019. – P. 14-16.