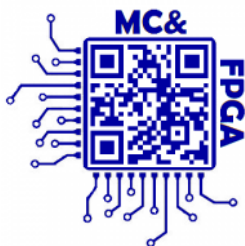


**VI International Scientific and Practical Conference**  
**«Theoretical and Applied Aspects of Device Development on**  
**Microcontrollers and FPGAs»**

**MC&FPGA-2024**

**June 27-28, 2024, Kharkiv, Ukraine**



**CALL FOR PAPERS**

**Dear colleagues!**

We invite you to participate in the VI International Scientific and Practical Conference "Theoretical and Applied Aspects of Device Development on Microcontrollers and FPGAs" MC&FPGA-2024, which will be held from 27 to 28 June 2024 in remote mode on the basis of the [Department of Microprocessor Technologies and Systems, Faculty of Information Radio Technology and Technical Information Protection](#) of the [Kharkiv National University of Radio Electronics](#), Kharkiv, Ukraine.

The relevance of the conference topics is due to the tendencies of the society's transition to the technologies of the Industry 4.0, 5.0 and IIoT, which put forward new requirements for the design and development of electronic devices. In particular, electronic equipment must meet the requirements: operation in real time, availability of universal interfaces, accounting for changes in external conditions, etc.

**The purpose of the conference**

Discussion of tasks and priority directions of scientific and technical development in the field of designing devices on microcontrollers and programmable logic integrated circuits; embedded systems; The Internet of

Things; industrial Internet of Things and more. Solving the problem of forming a better interaction of chains "education - science - production". Promoting the development of the innovation ecosystem and the development of scientific research and scientific and technical (experimental) developments.

**Topics of the conference:**

- Mathematical modeling of information signals and systems;
- Hardware Description Languages;
- Systems of computer aided design of devices on microcontrollers, microprocessors and FPGAs;
- Features of device development on microcontrollers and microprocessors;
- Aspects of the development of devices in the FPGA;
- Architecture and microarchitecture of specialized computing systems;
- Modern trends in the design of microprocessor technology;
- The problem of improving the quality of training specialists.

**Requirements for your abstracts preparation**

1. The abstracts prepared must be sent by June 15, 2024 by e-mail to [mcfpga@nure.ua](mailto:mcfpga@nure.ua).
2. The working language of the conference is English.
3. The volume of the abstracts must be from 2 to 4 full pages. The text of the abstract of the report should be prepared according to the [template](#) (template\_a4\_format\_2024.doc). The author is responsible for the quality and content of the theses.
4. After a successful review, a response is sent with the following information: "Abstracts of your report is taken".
5. Theses of reports will obtain digital identifiers DOI.
6. Publication of conference abstracts is free of charge.
7. Based on the results of the conference, theses will be published on the website [mcfpga.nure.ua](http://mcfpga.nure.ua).

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